Amendments to the Specification

Please replace the RELATED APPLICATIONS paragraph at page 1, lines 3 through 15 with the following amended paragraph:

This application is a Continuation of Application No. 10/390,532, filed on March 14, 2003, now abandoned, which is a Continuation of Application No. 09/808,619, filed on March 14, 2001, which is a Continuation of Application No. 09/293,477, filed April 16, 1999, now U.S. Patent No. 6,219,573, which is a Continuation of 08/682,260, filed July 17, 1996, now U.S. Patent No. 6,047,205, which is a Continuation of 08/333,205, filed November 2, 1994, now U.S. Patent No. 5,653,238, which is a Continuation-in-part of Application No. 07/832,109, filed February 6, 1992, now U.S. Patent No. 5,325,863 and of Application No. 07/889,052, filed May 22, 1992, now U.S. Patent No. 5,381,796 and of Application No. 07/760,006, filed September 13, 1991, now U.S. Patent No. 5,445,158, which is a Continuation-in-Part of Application No. 07/646,855, filed January 28, 1991, now U.S. Patent No. 5,199,436, which is a Divisional of Application No. 07/338,968, filed April 14, 1989, now U.S. Patent No. 5,012,813, which is a Continuation-in-part of Application No. 07/280,546, filed December 6, 1988, now U.S. Patent No. 4,993,419.

Please replace the paragraph at page 21, line 26 through page 22, line 2 with the following amended paragraph:

A schematic illustration of the electronics in the housing 14 of both embodiments of the present invention (Figures 1 and 3), for providing a temperature readout on display 16 in response to the signal from the thermopile, is presented in Figure 8. The system is based on a microprocessor [[73]] 75 which processes software routines included in read only memory within the processor chip. The processor may be a 6805 processor sold by Motorola.

Please replace the paragraph at page 22, line 24 through page 23, line 2 with the following paragraph:

At any time during the software routine of the microprocessor [[73]] 75, one of the four inputs may be selected by the select lines 78. The selected analog signal is applied to a multiple slope analog system 80 used by the microprocessor in an integrating analog-to-digital conversion 80. The subsystem 80 may be a TSC500A sold by Teledyne. It utilizes the reference voltage VRef from a reference source 82. The microprocessor [[73]] 75 responds to the output from the converter 80 to generate a count indicative of the analog input to the convertor.

Please replace the paragraph at page 23, lines 7 through 14 with the following paragraph:

When the switch 22 on the housing is pressed, it closes the circuit from the battery [[78]] through resistors R5 and R6 and diode D1 to ground. The capacitor C1 is quickly charged, and field effect transistor T1 is turned on. Through transistor T1, the V+ potential from the storage cell [[78]] is applied to a voltage regulator [[86]] 88. The regulator [[86]] 88 provides the regulated +5 volts to the system. It also provides a reset signal to the microprocessor. The reset signal is low until the +5 volt reference is available and thus holds the microprocessor in a reset state. When the +5 volts is available, the reset signal goes high, and the microprocessor begins its programmed routine.